

	Type	L #	Hits	Search Text	Dbs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	19	((substrate or board) same channel\$1 same cooling ) and layer\$1	JPO	2002/07/1 2 09:09			0
2	BRS	L2	105	((substrate or board) same channel\$1 same cooling	JPO	2002/07/1 2 09:25			0
3	BRS	L3	86	2 not 1	JPO	2002/07/1 2 08:57			0
4	BRS	L4	23	((substrate or board) same channel\$1 same cooling ) and layer\$1	EPO	2002/07/1 2 09:25			0
5	BRS	L5	6	((substrate or board) same channel\$1 same cooling ) and layer\$1	IBM_ TDB	2002/07/1 2 09:21			0
6	BRS	L6	60	((substrate or board) same channel\$1 same cooling ) and layer\$1	DERW ENT	2002/07/1 2 09:35			0
7	BRS	L7	50	((substrate or board) same channel\$1 same cooling )	IBM_ TDB	2002/07/1 2 09:21			0
8	BRS	L8	44	7 not 5	IBM_ TDB	2002/07/1 2 09:21			0
9	BRS	L9	103	((substrate or board) same channel\$1 same cooling	EPO	2002/07/1 2 09:25			0
10	BRS	L10	80	9 not 4	EPO	2002/07/1 2 09:25			0
11	BRS	L11	254	((substrate or board) same channel\$1 same cooling )	DERW ENT	2002/07/1 2 09:35			0
12	BRS	L12	194	11 not 6	DERW ENT	2002/07/1 2 09:35			0

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
1	BRS	1018	(substrate or board) same channel\$1 same cooling	USPAT	2002/07/12 08:57		
2	BRS	715	((substrate or board) same channel\$1 same cooling ) and layer\$1	USPAT	2002/07/12 08:49		